

Electronic Acknowledgement Receipt

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Application Number:	10708056
International Application Number:	
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Title of Invention:	METHOD AND SYSTEM TO VERIFY A CIRCUIT DESIGN BY VERIFYING CONSISTENCY BETWEEN TWO DIFFERENT LANGUAGE REPRESENTATIONS OF A CIRCUIT DESIGN
First Named Inventor/Applicant Name:	Edmund M. Clarke
Customer Number:	24239
Filer:	Charles L. Moore/Katie Efland
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)

1	Issue Fee Payment (PTO-85B)	Issue_Fee_Payment.pdf	173255	no	1
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8375	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				181630	
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